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20987 7590 02/11/2005

VOLENTINE FRANCOS, & WHITT PLLC
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RESTON, VA 20190

05/11/2005 MBEYENE2 00000200 10773464

01 FC:1501	1400.00 DP
02 FC:1504	300.00 DP
03 FC:8001	12.00 DP

APPLICATION NO. FILING DATE

FIRST NAMED INVENTOR

ATTORNEY DOCKET NO.

CONFIRMATION NO.

10/773,464

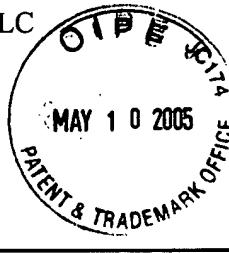
02/09/2004

Hi-Choon Lee

SEC.1108

3599

TITLE OF INVENTION: SEMICONDUCTOR MEMORY DEVICE HAVING HIGH-SPEED INPUT/OUTPUT ARCHITECTURE



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APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/11/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
HOANG, HUAN	2827		365-233000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1 Volentine Francos & Whitt

PLLC

2 _____

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(A) NAME OF ASSIGNEE

Samsung Electronics Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Suwon-si, Gyeonggi-do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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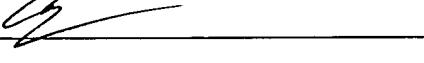
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- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature 

Date May 10, 2005

Typed or printed name Adam C. Volentine

Registration No. 33289

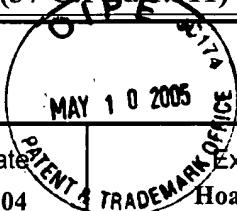
This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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 (37 CFR 1.311)

Docket No.
 SEC.1108

Applicant(s): Hi-Choon Lee



Application No. 10/773,464	Filing Date 02/09/2004	Examiner Hoang, Huan	Customer No. 20987	Group Art Unit 2827	Confirmation No. 3599
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Invention: SEMICONDUCTOR MEMORY DEVICE HAVING HIGH-SPEED INPUT/OUTPUT ARCHITECTURE

Mail Stop Issue Fee
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Dated: MAY 10, 2005

ADAM C. VOLENTE
 REG. NO. 33289

VOLENTE FRANCOS & WHITT, PLLC
 11951 FREEDOM DRIVE, SUITE 1260
 RESTON VA 20190

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